电视/电视机顶盒的电源设计解析 2017.11 🔱 Texas Instruments

TV & Monitor Display Solutions - LCD Bias, Level Shifters, Gamma Buffers

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DPP AMOLED and LCD Display Solutions



Typical LCD Display Subsystem









LS to specific panel/source driver. >157Mu shipped & growing....



TPS65177 + TPS65197 on top panel maker reference design

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TPS65175C: Fully Programmable LCD Bias IC for GIP TV with Integrated 12-Ch Level Shifters and 6-Ch Gamma Buffers

A ATILITAS	

- 8.6V to 14.7V Input Voltage Range
- Boost Converter VDD: 12.7V...19V (6-Bit)
- Integrated Input-to-Output Isolation Switch
- Buck Converter HVDD: VDD Tracking
- Buck Converter VCC: 1.6V...2.0V & 3.0V...3.6V (4-Bit), 1.8V dft
- Positive Charge Pump VGH:
- 19V...34V for Low Temperature (4-Bit)
 17V...32V for High Temperature (4-Bit)
- Negative Charge Pump VGL: -1.8V...-8.1V (6-Bit)
- 6-Ch Gamma Buffer:
 - 3-Ch: VDD...HVDD (9-Bit)
 - 3-Ch: HVDD...GND (9-Bit)
- 9-Bit VCOM Reference
- 3-Bit VCOM Gain
- VCOM_DAC Output for External Operational Amplifier Reference (TPS65175C specific)
- Temperature Compensation for VGH and VCOM
- Reset Signal with Programmable Delay Time
- Programmable Sequencing Delays (4 × 3-Bit)
- Thermal Shutdown
- 12-Channel Level Shifters:
 - Supports Forward and Reverse Operation
 - Abnormal Operation Detection
- 56-Pin 7-mm × 7-mm QFN Package

Applications

- LCD GIP/GOA TVs
- LCD GIP/GOA Monitors

Benefits

- 1st TI device integrating the LCD bias, the 12ch level shifters and the gamma buffer, combining TPS65178 & TPS65198, saving the 4x4mm² package of TPS65198 + routing
- Very small device that use 3 high frequency converters which can be used with chip inductors (cheaper & smaller than wire wounded inductors)
- Better control of startup of the boost and short circuit & reduces the BOM
- I2C Programmability allows for :
 - Easy PCB layout (no resistor divider to set output voltages)

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Flexible solution (1 BOM for all panels) .



TPS65177: Can be used in conjunction with the TPS65197 level shifter Fully PC programmable 6-Ch LCD Bias IC for all size TVs including Gate Pulse Modulation

Features	Benefits
 Gate Pulse Modulation (GPM) Temperature Compensation for VGH 40-Pin 6x6 mm QFN Package I2C compatible Interface Input Voltage 8.6V14.7V Integrated VAVDD Isolation Switch 6-bit Boost Converter VAVDD: 13.5V19.8V 3-bit prog. Switch Current Limit up to 4.25A 4-bit prog. High Voltage Stress Mode 1-bit prog. Soft-start 	 Enables non GIP TVs Enables GIP TVs Polyvalent LCD bias enabling all TVs independently of the panel technology used Very small device that use 3 high frequency converters which can be used with chip inductors (cheaper & smaller than wire wounded inductors) I2C Programmability allows for : Easy PCB layout (no resistor divider to set output voltages) Flexible solution (1 BOM for all panels)
 6-bit Sync. Buck VHAVDD: 4.8V11.1V 1.7A Switch Current Limit 4-bit prog. High Voltage Stress Mode 4-bit Buck Converter VIO: 2.2V3.7V 3A Switch Current Limit 5-bit Sync. Buck VCORE: 0.8V3.3V 2.5A Switch Current Limit 4-bit Garge Rump Constraints 4-bit Corrent Limit 4-bit Garge Rump Constraints 4-bit Current Limit 	J.6V14.7V Isolation Switch Buck Converter 1 V _{AVDD} Buck Converter 2 V _o (Synchronous) V _{CORE}
• 4-bit Unarge Pump Controller VGH: 20V40V	and Brock Conventor 2





TPS65197 / 97B:

Works perfectly with the TPS65177 bias power solution

8-Channel Level-Shifter supporting 2- & 3-Channel Charge-Sharing & Panel Discharge

Features	Benefits
 8- Channel Level-Shifter Supporting 6 x CLK, STV, RESET Supply Voltage Range: 16.5 to 45 V VGH Range (High Output-Voltage Level): 16.5 to 45 V VGL Range (Low Output-Voltage Level): -20 to 0 V 	 Charge-Sharing for Power Savings Improved Picture Quality by Charge-Sharing Active Panel Discharge Avoids Panel Flash at Shutdown Flexibility by three Charge-Sharing Options Latched Shut-down Detection Results in Proper Discharge
 Selectable Charge-Sharing Options No Charge-Sharing 2-channel Charge-Sharing 3-channel Charge-Sharing 2-Channel Panel Discharge T-CON Failure Detection TPS65197 (Failure Recovery: Reset Every Frame) 	O SEL_CS CLKOUT1 V(cLK1) O CLKIN1 CS_1 V(cLK2) O CLKIN2 V(cLK0UT2) V(cLK3) O CLKIN3 CLSUT2 V(cLK3) O CLKIN4 CLSUT3 V(cLK6) O CLKIN5 CLKOUT3 V(cLK6) O CLKIN5 CLKOUT4 V(cLK6) O CLKIN5 CLKOUT4
 TPS65197B (no Reset) 	CLKOUT5 V(CLKOUT5) CLKOUT6 V(CLKOUT6)

- Shut-down Detection (Latched)
- Supports 100 kHz Clock Operating Frequency
- 28-pin 4x4 mm QFN package

Applications

- PC/Notebooks
- TV
- General Gate-in-Panel (GIP) TFT-LCD Applications



STVOUT RESETOUT

STVIN RESETIN

V_(STV) O

(RESET) O



-O V(STVOUT) -O V(RESETOUT)

TPS65198: Can be used in conjunction with the TPS65178 bias 13-Channel Level Shifter with Op-Amp for LCD TVs and Monitors

Benefits Features • Built-in state machine Six CLK outputs with Gate Shaping Integrated state machine reduces the number of VST and RESET outputs connections between T-CON and level shifter from 12 to 5 ODD and EVEN outputs Package is 36% smaller than existing devices (4x4 mm vs. 5x5 VGH_F and VGH_R outputs mm) and it includes additional VCOM buffer function 28-Pin 4×4 mm QFN Package . Panel DISCHARGE output Supports forward and reverse operation . Abnormal operation detection Level Shifters Supports all display resolutions with Gate Shaping CLK1-CLK6 High-speed operational amplifier for buffering the VCOM voltage high slew rate GCLK MCLK high output current State Machine GST Level Shifters VST RESET . wide bandwidth without Gate Shaping EO **BI-SCAN** VGH_F VGH_R Level Shifters ODD without Gate Shaping EVEN Applications Panel Discharge VSENSE DISCHARGE LCD TVs and monitors using GIP technology POS **Operational Amplifier** ► OUT NEG

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BUF16821: Multi-Channel 10-Bit Multi-Time Programmable Voltage Source

Features			
10-Bit DACs			
 16x MTP Memory 			
 Double Buffered Registers 			
2 Banks with Bank Select			
 Excellent Output Current Drive: 			

- - 16 Gamma Channels: 30mA
 - 2 VCOM: 100mA

Benefits

- Non-Volatile Memory allows for system calibration
- Bank Select allows for switching output bias levels on the . fly
- Double buffering allows time for programming each . channel, then loading simultaneously



Thank you



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